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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

Application Number	10674642
Filing Date	2003-09-30
First Named Inventor	Levine et al.
Art Unit	2192
Examiner Name	Chou, Andrew Y.
Attorney Docket Number	AUS920030484US1

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5657253		1997-08-12	Dreyer et al.	
	2	5991708		1999-11-23	Levine et al.	
	3	5564015		1996-10-08	Bunnell	
	4	5450349		1995-09-12	Brown et al.	
	5	5987598		1999-11-16	Levine et al.	
	6	6009514		1999-12-18	Henzinger et al.	
	7	6112317		2000-08-29	Berc et al.	
	8	5896538		1999-04-20	Blandy et al.	

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	9	5355487		1994-10-11	Keller et al.	
	10	6948032	B2	2005-09-20	Kadambi et al.	

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	1	"CPU Cache", Wikipedia, pp. 1-14 retrieved 11/1/06 <a href="http://en.wikipedia.org/wiki/CPU_cache">http://en.wikipedia.org/wiki/CPU_cache</a>	<input type="checkbox"/>
	2	AMMONS et al., "Exploiting Hardware Performance Counters with Flow and Context Sensitive Profiling", 1997, ACM, pp. 85-96.	<input type="checkbox"/>

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See attached certification statement.  
 Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.  
 None

### SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Gerald H. Glanzman/	Date (YYYY-MM-DD)	2006-12-04
Name/Print	Gerald H. Glanzman	Registration Number	25035

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